MOHAMMADREZA (SIAVASH) KATEBZADEH

The University of Edinburgh, Informatics Forum 1.48/2, EH8 9AB, United Kingdom
Email ◇ Website ◇ Github ◇ Scholar ◇

WORK EXPERIENCE

Postdoctoral Research Associate Since 2024
The University of Edinburgh *Edinburgh, United Kingdom*
Project: Hardware-accelerated reliable replication protocols.

CPU Architecture Intern Jul-2023 December-2023
Huawei Technologies Research & Development (UK) Ltd *Edinburgh, United Kingdom*
Project: Address translation optimization in the RISC-V architecture.

Research Assistant Jan 2023 - Jul 2023
The University of Edinburgh *Edinburgh, United Kingdom*

Postgraduate Researcher 2017 – 2022
The University of Edinburgh *Edinburgh, United Kingdom*
Projects:
– Saba, an application-aware bandwidth allocation scheme designed for RDMA-based datacenter networks and optimized for bandwidth-hungry frameworks (e.g., Apache Spark).
– RPerf, an accurate performance measurement system designed for RDMA-based networks.

Intern 2016
Parallel Systems Architecture Lab, EPFL *Lausanne, Switzerland*
Projects:
– Implementation of CPU timing on QFlex system simulator.
– Detection and elimination of various sources of non-deterministic behavior of QEMU.

Researcher 2014
Software Systems Laboratory, Shiraz University *Shiraz, Iran*
Project: Research on performance optimization of big data workloads on GPU.

EDUCATION

Ph.D., The University of Edinburgh, Edinburgh, United Kingdom 2017 – 2022
Computer Architecture & Network Communication
Supervisor: Prof. Boris Grot
Thesis title: Application-Centric Bandwidth Allocation in Datacenters

M.Sc., Shiraz University, Shiraz, Iran 2015 – 2017
Software Engineering
Supervisor: Dr. Farshad Khunjush
Thesis title: Implementation of a Deterministic Functional Simulator

B.Sc., Shiraz University, Shiraz, Iran 2010 – 2014
Software Engineering
Advisor: Dr. Reza Sameni

RELATED PUBLICATIONS

Saba: Rethinking Datacenter Network Allocation from Application's Perspective   2023
M.R.S Katebzadeh, P. Costa, B. Grot
In the European Conference on Computer Systems (EuroSys)

Evaluation of an InfiniBand Switch: Choose Latency or Bandwidth, but Not Both   2020
M.R.S. Katebzadeh, P. Costa, B. Grot
In the International Symposium on Performance Analysis of Systems and Software (ISPASS)

Hermes: Fast, Fault-Tolerant and Linearizable Data Replication   2020
A. Katsarakis, V. Gavrielatos, M.R.S. Katebzadeh, A. Joshi, B. Grot, V. Nagarajan, A. Dragojevic
In the International Conference on Architectural Support for Programming Languages and Operating Systems (ASPLOS)

WORKSHOPS

Reliable Replication Protocols on SmartNICs   2025
M.R.S Katebzadeh, A. Katsarakis, B. Grot
In Workshop on Principles and Practice of Consistency for Distributed Data (PaPoC), co-located with EuroSys

Bankrupt Covert Channel: Turning Network Predictability into Vulnerability   2020
D. Ustiugov, P. Petrov, M.R.S. Katebzadeh, B. Grot
In USENIX Workshop on Offensive Technologies (WOOT), co-located with USENIX Security

Smart Priority Assignment in Datacenter Networks   2020
M.R.S. Katebzadeh, P. Costa, B. Grot
In the second Young Architect Workshop (YArch), co-located with ASPLOS

TECHNICAL SKILLS

Programming   Rust, C/C++, Python, Java, Dart, JavaScript, React, Flutter
Miscellaneous   Proxmox VE, Docker, Qemu, Git, Emacs, LaTeX, Shell Scripting

COMMUNITY SERVICES

- **EuroSys'2026**
  – Member of the program committee at the 21st European Conference on Computer Systems.

- **HPCA'2024**
  – Web co-chair at the 30th International Symposium on High-Performance Computer Architecture.
  – Member of the Artifact Evaluation committee.

- **EuroSys'2023**
  – Member of the Artifact Evaluation committee at the 18th European Conference on Computer Systems.

- **MICRO'2022**
  – Submission co-chair at the 55th International Symposium on Microarchitecture.
  – Implementation of PiCkeR, a conference management toolkit for program chairs.

- **ISCAConf Website**
  Maintainer of the main ISCA conference website and archive at https://iscaconf.org.